

## REMARKS

The Office Action dated January 5, 2009 indicated that claims 4-10 would be allowable upon rewriting to include limitations of the base and intervening claims, and that claims 1-3 and 11-12 stand rejected under 35 U.S.C. §102(e) over Jacobson (U.S. Patent No. 6,499,124). Applicant appreciates the allowance of claims 13-16. Applicant fully traverses the rejections for lack of correspondence, *e.g.*, entirely different structure asserted (“security circuit”) in the ‘124 reference. Notwithstanding, Applicant has amended the claims in a manner that facilitates prosecution without prejudice and without acquiescing.

Consistent with the reasons for allowing claim 4, Applicant has amended claim 1, thereby rendering each of claims 1-3 in allowable form. Support for the language in claim 1 is found, *e.g.*, in claim 4 and paragraphs 38-42 of Applicant’s published version of the application.

Claims 4 and 7 have been amended to include the subject matter recited in claim 1 and to include a first logic circuit as in claim 2; thereby rendering claims 4-6 and claims 7-10 in allowable form.

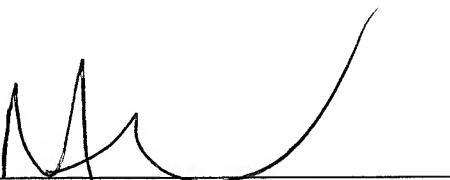
Applicant respectfully traverses the rejection of claims 11 and 12 over the ‘124 reference, for lack of correspondence to Applicant’s invention as a whole (*e.g.*, “security circuit” of the ‘124 reference). The amendment to claim 11 further emphasizes some of the differences. Support therefor is found, *e.g.*, in Applicant’s specification at paragraphs 39-42.

In view of the above, Applicant believes that each of the rejections/objections has been overcome and the application is in condition for allowance. Should there be any remaining issues that could be readily addressed over the telephone, the Examiner is asked to contact the agent overseeing the application file, Peter Zawilski, of NXP Corporation at (408) 474-9063.

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